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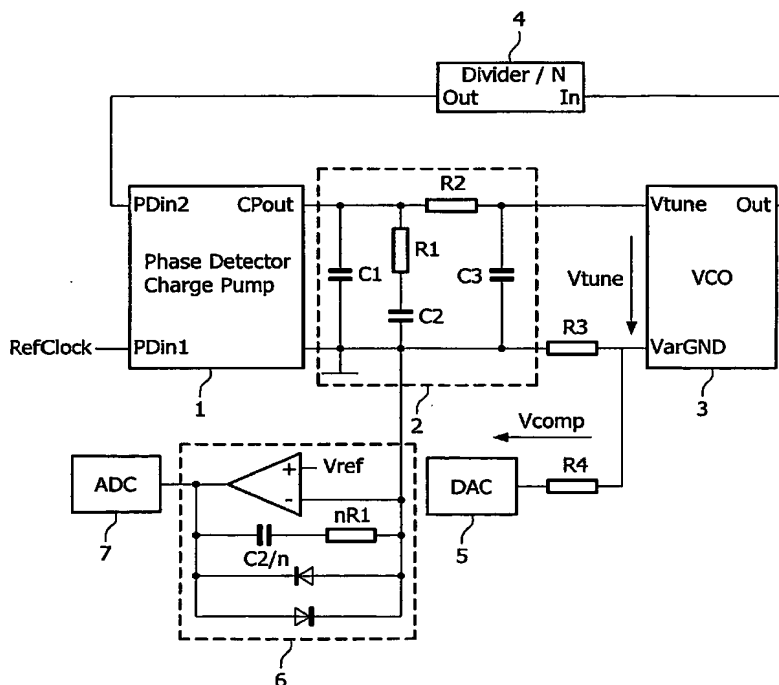
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(54) Title: **METHOD AND ARRANGEMENT FOR INTERFERENCE COMPENSATION IN A VOLTAGE-CONTROLLED FREQUENCY GENERATOR**



(57) Abstract: The invention, which relates to a method and an arrangement for interference compensation in a phase-locked loop comprising a voltage-controlled frequency generator, wherein the frequency generator is tuned to a nominal frequency by a tuning voltage  $V_{tune}$  and whose actual frequency is compared with a reference frequency by means of a frequency comparison and is re-adjusted if a deviation is detected via the frequency comparison, in which case, in the event of interference, the tuning voltage  $V_{tune}$  is changed by an interference voltage  $V_{stor}$  that depends on the interference event, and thus a frequency deviating from the nominal frequency is generated, which deviating frequency is corrected again by the phase locked loop, is based on the object to provide a method and an arrangement for interference compensation in a phase-locked loop comprising a voltage-controlled frequency generator, with which a deviation from a predefined nominal

frequency is avoided if known interference events occur. The object is achieved in accordance with the invention in a method whereby, if a known interference event occurs, a voltage  $V_{stor}$  which compensates for the interference voltage  $V_{comp}$ , is generated in synchronism with this with sign inversion and is superimposed on the interference voltage  $V_{stor}$ .



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